COMP 1588 Computer Systems Architectures Header ID Contribution 100% of Course

Coordinator P.A.Smith. Release Date Deadline Date

This coursework should take an average student who is up-to-date with tutorial work approximately 50 hours

Learning Outcomes A,B,C,D

Plagiarism is presenting somebody else's work as your own. It includes: copying information directly from the Web or books without referencing the material; submitting joint coursework as an individual effort; copying another student's coursework; stealing coursework from another student and submitting it as your own work. Suspected plagiarism will be investigated and if found to have occurred will be dealt with according to the procedures set down by the University. Please see your student handbook for further details of what is / isn't plagiarism. Details are also on the Student Intranet.

All material copied or amended from any source (e.g. internet, books) must be referenced correctly according to the reference style you are using.

Your work will be submitted for plagiarism checking. Any attempt to bypass our plagiarism detection systems will be treated as a severe Assessment Offence. By handing in your coursework you are confirming that it has not, in whole or part, been presented elsewhere for assessment.

In addition, you are confirming that

•All material which has been copied has been clearly identified as such by being placed inside quotation marks and a full reference to the source has been provided

•Any material which has been referred to or adapted has been clearly identified and a full reference to the source has been provided

•Any work not in quotation marks is in your own words

•You have not shared your work with any other student

•You have not taken work from any other student

•You have not paid anyone to do your work or employed the services of an essay or code writing agency

Coursework Specification

Laboratories - 70%

•Electrical Principles (15%)

•Digital Logic (15%)

•Assembly Problem (40%)

Laboratory work must be uploaded (pdf format only) within six days of completion of the supervised session, and a paper copy handed in to the tutor at start of the following laboratory - late work will be marked to zero. Filename should indicate the laboratory exercise undertaken e.g. lab\_electrical\_principles.pdf.

Laboratory Reports - Marks will be awarded for:

•Completeness

•Relevant content which answers all questions and clearly explains any diagrams or graphs

•Coherent discussions and conclusions demonstrating an understanding of the concepts

•Reflection

•Referencing - should adhere to The Harvard System

•Correct English and Grammar

Theory Test - 30%

Test is based on questions from the following exercises on the course home page.

•Number Representation, Number bases, and Data Representation

•CPU Architecture

•Boolean Algebra, Logic Gates

•Timing Diagrams

•Flow Charts

•Finite State Machines

Course schedule can be found here: <http://staffweb.cms.gre.ac.uk/~sp02/Collaborations/comp1588schedule.htm>